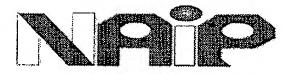
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Art Unit: 1756

Fax: (571) 273-8300

From: Winston Hsu, Registration No. 41,526

Serial No.: 10/711,198

Attorney Docket No.: NAUP0493USA4

Subject: Information Disclosure Statement (IDS)

Total Pages: 81 pages (including cover page)

Winston Hsu 2005/11/24

From: 8064986673

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Printed name			Winston	Hsu						
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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will very depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Fees pursuant to the Consolidated Appropriations Act. 2005 (H.R. 4818).	Application Number	10/711,198]
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For FY 2005	First Named Inventor	Jiunn-Ren Hwang		ENTRAL FAX CENTER
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Applicant claims small entity status. See 37 CFR 1.27	Art Unit	1756] NOV 2 4 2005
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METHOD OF PAYMENT (check all that apply)				
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Deposit Account Deposit Account Number: 50-3105		ame: North America Intelle	ectual Property Corp.	•
For the above-identified deposit account, the Director is he	reby authorized to: (check	all that apply)		
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SUBMITTED BY				~
	Registration No. 41,520 (Attorney/Agent)	Telephone 3	02-729-1562	
Name (Print(Type) Winston Hou	the second of the second	Date 11/2/		1

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) on application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the includual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandría, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandría, VA 22313-1450.

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Substitute for form 1449/PTO	Complete if Known			
	Application Number	10/711,198		
INFORMATION DISCLOSURE	Filing Date	09/01/2004		
	First Named Inventor	Jiunn-Ren Hwang		
STATEMENT BY APPLICANT	Art Unit	1756		
(Use as many sheets as necessary)	Examiner Name	YOUNG, CHRISTOPHER G		
than 1 of 1	Attorney Docket Number	NAUP0493USA4		

			U. S. PATENT D			
Examiner Initials*	Cite . No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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		FORE	IGN PATENT DOCU	MENTS		
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	1	JP 5-19446	01-29-1993		all	+
	2	JP 6-266095	09-22-1994		all	+
	3	JP 11-102063	04-13-1999		all	+
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. *Applicant's unique citation designation number (optional). *See Kinds Codes of USPTO Patent Documents of www.uspto.nov or MPEP 901.04. *Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). *For Japanese patent document, in indication of the year of the reign of the Emperor must precede the serial number of the patent document. *And of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. *Applicant is to place a check mark here if English tanguage Translation is attached.

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jiunn-Ren Hwang, Jui-Tsen Huang, Chang-Jyh Hsich

Filing Date: 09/01/2004

Art Unit:

1756

Serial No.: 10/711,198

Docket No.: NAUP0493USA4

Examiner: YOUNG, CHRISTOPHER G

10 Confirmation No.: 5197

Title: OPTICAL PROXIMITY CORRECTION METHOD

To:

Commissioner for Patents

15

P.O. BOX 1450

Alexandria, VA 22313-1450

Subject: Information disclosure statement under 37 C.F.R.§1.56.

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Dear Sir:

This is an Information Disclosure Statement in accordance with the duty to disclose information material to patentability under 37 CFR §1.56. The applicant wishes to make of record the document(s) listed on the accompanying form PTO/SB/08.

Since this IDS is filed after the mailing date of the first Office action but before final Office action, consideration of the information disclosure statement is hereby requested according to 37 CFR §1.97(c).

That each item of information contained in the information

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disclosure statement was first cited in an Office communication mailed on 2005/09/20 for the counterpart Japan patent application No. 2003-132557, which are no more than three months prior to the filing of the information disclosure statement.

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According to the requirement set forth in 37 C.F.R.§1.98 and M.P.E.P. 609, the applicants are submitting a copy of the cited reference (Japan patents publication No. 11-102063, 5-19446, and 6-266095) and their English language abstracts hereinafter.

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The abstract of the application (Japan patents publication No. 11-102063) is hereby presented to fulfill the information disclosure statement requirement set forth in 37 C.F.R.§1.98 and M.P.E.P. 609. The English language abstract of the application (Japan patents publication No. 11-102063) is listed as follows:

PROBLEM TO BE SOLVED:

To improve the transfer accuracy of intricate and fine patterns formed on an optical mask by patterning the phase shift film on a mask substrate to the shapes resembling the plane shape with the circuit patterns to be formed on a semiconductor wafer or the inversion patterns thereof.

SOLUTION:

Metallic layers 3 are patterned and formed to prescribed shapes on the main surface of the mask substrate 2 constituting a mask 1a. The original picture of integrated circuit patterns is composed of light shielding regions A and transmission regions B. The one transmission region B comprises a part coated with a transparent film (a) and a part not formed with this transparent film 4a. At the time of exposure, a phase difference of 180° is induced between the phase of the light transmitted through the transparent film 4a out of the light which is cast on the mask 1a and is transmitted through the one transmission region B

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and the phase of the light transmitted through the ordinary transmission regions B. These light rays weaken each other in the boundary parts between the light shielding regions A and the transmission regions B and, therefore, unsharpness is reduced.

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The abstract of the application (Japan patents publication No. 5-19446) is hereby presented to fulfill the information disclosure statement requirement set forth in 37 C.F.R.§1.98 and M.P.E.P. 609. The English language abstract of the application (Japan patents publication No. 5-19446) is listed as follows:

PURPOSE:

To provide the reduction stepping method which allows exposing of diversified and fine patterns in the process for production of the semiconductor device or semiconductor integrated circuit device.

CONSTITUTION:

This method consists in executing reduction stepping by using a phase shift mask provided with prescribed correction patterns 101b at the ends of a mask pattern region of a specified mode or in the boundary parts of the mask pattern regions of plural modes. Since an end effect, etc., are offset by the correction patterns, the exposing of the diversified and fine patterns is executed.

The abstract of the application (Japan patents publication No. 6-266095) is hereby presented to fulfill the information disclosure statement requirement set forth in 37 C.F.R.§1.98 and M.P.E.P. 609. The English language abstract of the application (Japan patents publication No. 6-266095) is listed as follows:

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PURPOSE:

To eliminate a defect generated from the non-uniformity of a thin layer

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of chromium of a damping phase shift type reticle.

CONSTITUTION:

This reticule 20 is provided with a secondary resolution pattern 22 in an area other than a graphic part to be formed. By the secondary resolution pattern 22, the radiation light of practically uniform and attenuated intensity is transmitted. By the graphic part, phase shift is performed to the secondary resolution pattern 2 as the following. That is, the phase of light passed through the graphic parts is shifted for about 180° to the phase of the radiation light passed through the secondary resolution pattern 2 surrounding the graphic parts and attenuated.

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Respectfully submitted,

20 Weinton bars

Date: November 24, 2005

Winston Hsu, Patent Agent No. 41,526

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